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STATUS REQUEST		Docket Number CM03422J					
In re Application of STENGEL, ROBERT, ET AL							
Application Number	10/036,550	File Date	12/21/2001				
Title A METHOD AND STRUCTURE FOR INTEGRATED CIRCUIT INTERFERENCE ISOLATION ENHANCEMENT							
Group Art Unit	NOT ASSIGNED	Examiner	NOT ASSIGNED				
Status of the above-identified application is requested. The last paper(s) received by Applicant(s) was: <table border="0"><tr><td><input checked="checked" type="checkbox"/></td><td>Official Filing Receipt</td></tr><tr><td><input type="checkbox"/></td><td>Other</td></tr></table> The last paper(s) submitted by Applicant(s) was: <input type="checkbox"/> Since the transmittal/receipt of said paper(s), no other communications have been received from the United States Patent and Trademark Office.				<input checked="checked" type="checkbox"/>	Official Filing Receipt	<input type="checkbox"/>	Other
<input checked="checked" type="checkbox"/>	Official Filing Receipt						
<input type="checkbox"/>	Other						
Attorney or Agent		Attorney					
Printed Name	Barbara R. Doutre	Registration Number	39,505				
Customer Number:	24273						
Signature							
Date	7/18/03						
<p align="center"><u>CERTIFICATE OF MAILING</u></p> <p>I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first-class mail in an envelope addressed to the Mail Stop STATUS, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450.</p>							
Name	Maria E. Rodriguez						
Signature		Date	7/18/03				